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**2** [A dynamic binary instrumentation engine for the ARM architecture](#)

Kim Hazelwood, Artur Klauser

 October 2006 **CASES '06:** Proceedings of the 2006 international conference on embedded systems

**Publisher:** ACM

 Full text available: [PDF](#) (455.03 KB) Additional Information: [full citation](#), [abstract](#)
**Bibliometrics:** Downloads (6 Weeks): 11, Downloads (12 Months): 90, Downloads (All Time): 90

Dynamic binary instrumentation (DBI) is a powerful technique for analyzing embedded architectures. Numerous DBI frameworks have been developed for general-purpose architectures, but few have been developed for embedded architectures. This paper presents a new DBI framework for embedded architectures. The framework is designed to be easy to use and to integrate with existing development tools. It is implemented as a library that can be linked into existing applications. The framework is designed to be easy to use and to integrate with existing development tools. It is implemented as a library that can be linked into existing applications.

**Keywords:** binary instrumentation, dynamic translation, embedded architectures

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October 2008 Communications of the ACM

**Publisher:** ACM

 Full text available: [Digital Edition](#), [PDF](#) (7.16 MB) Additional Information: [full citation](#)
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**4** [Communications of the ACM: Volume 52 Issue 10](#)

October 2009 Communications of the ACM

**Publisher:** ACM

 Full text available: [Digital Edition](#), [PDF](#) (7.38 MB) Additional Information: [full citation](#)
**Bibliometrics:** Downloads (6 Weeks): 490, Downloads (12 Months): 490, Downloads (All Time): 490

**5** [Communications of the ACM: Volume 53 Issue 4](#)


April 2010 Communications of the ACM

**Publisher:** ACM

 Full text available: [Digital Edition](#), [PDF](#) (14.07 MB) Additional Information: [full citation](#)

**Bibliometrics:** Downloads (6 Weeks): 6452, Downloads (12 Months): 6452, Downloads (All Time): 6452

- 6 [Incremental Support Vector Learning: Analysis, Implementation and](#)  
 Pavel Laskov, Christian Gehl, Stefan Krüger, Klaus-Robert Müller  
 December 2006 **The Journal of Machine Learning Research** . Volume 7  
**Publisher:** MIT Press


Full text available:  [Pdf](#) (297.83 KB)

**Additional Information:** [full citation](#), [abstract](#)

**Bibliometrics:** Downloads (6 Weeks): 8, Downloads (12 Months): 115, Downloads (All Time): 115


Incremental Support Vector Machines (SVM) are instrumental in practice, as they focus on the design and analysis of efficient incremental SVM learning algorithms that are stable and robust ...

- 7 [Beyond streams and graphs: dynamic tensor analysis](#)

 Jimeng Sun, Dacheng Tao, Christos Faloutsos

August 2006 **KDD '06: Proceedings of the 12th ACM SIGKDD international conference on Knowledge discovery and data mining**

**Publisher:** ACM  [Request Permissions](#)


Full text available:  [Pdf](#) (1.14 MB)

**Additional Information:** [full citation](#), [abstract](#)

**Bibliometrics:** Downloads (6 Weeks): 21, Downloads (12 Months): 159, Downloads (All Time): 159

How do we find patterns in author-keyword associations, evolving over time? This paper focuses on the design and analysis of efficient incremental SVM learning algorithms that are stable and robust ...

- 8 [Proceedings of the 2008 ACM symposium on Applied computing](#)

 Roger L. Wainwright, Hisham M. Haddad


March 2008 **SAC '08: Proceedings of the 2008 ACM symposium on Applied computing**  
**Publisher:** ACM

**Additional Information:** [full citation](#), [abstract](#)

**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (All Time): n/a

Welcome to the 23rd Annual ACM Symposium on Applied Computing (SAC) which is a forum for computer scientists, engineers, and practitioners seeking innovative solutions to real-world problems. This year, the conference ...

- 9 [A regulated transitive reduction \(RTR\) for longer memory race records](#)

 Min Xu, Mark D. Hill, Rastislav Bodik

November 2006 **ASPLOS-XII: Proceedings of the 12th international conference on Architectural Support for Programming Languages and Operating Systems**

**Publisher:** ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (524.02 KB)

**Additional Information:** [full citation](#), [abstract](#)

**Bibliometrics:** Downloads (6 Weeks): 3, Downloads (12 Months): 71, Downloads (All Time): 71

Now at VMware. Multithreaded deterministic replay has important applications in intrusion analysis. Memory race recording is a key technology for multithreaded debugging ...

**Keywords:** determinism, multithreading, race recording

Also published in:


October 2006 **SIGARCH Computer Architecture News** Volume 34 Issue 5  
 October 2006 **SIGOPS Operating Systems Review** Volume 40 Issue 5  
 November 2006 **SIGPLAN Notices** Volume 41 Issue 11

# 10 Program locality analysis using reuse distance

Yutao Zhong, Xipeng Shen, Chen Ding

August 2009 **Transactions on Programming Languages and Systems**

**Publisher:** ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (2.15 MB)

[Additional Information: full citation, abs](#)

**Bibliometrics:** Downloads (6 Weeks): 48, Downloads (12 Months): 333, Downl

On modern computer systems, the memory performance of an applicati  
 execution, locality-correlated measures like average miss rate or worki  
*distance*—the ...


**Keywords:** Program locality, reuse distance, stack distance, training-bi

# 11 The Conquest file system: Better performance through a disk/persist

An-I Andy Wang, Geoff Kuenning, Peter Reiher, Gerald Popek

August 2006 **Transactions on Storage (TOS)** , Volume 2 Issue 3

**Publisher:** ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (1.34 MB)

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**Bibliometrics:** Downloads (6 Weeks): 16, Downloads (12 Months): 151, Downl

Modern file systems assume the use of disk, a system-wide performanc  
 caching and RAM file systems either impose high overhead to access m  
 achieve data persistence ...


**Keywords:** Persistent RAM, file systems, performance measurement, s

# 12 Practical escape analyses: how good are they?

Kyungwoo Lee, Xing Fang, Samuel P. Midkiff

June 2007 **VEE '07: Proceedings of the 3rd international conference on '**

**Publisher:** ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (626.23 KB)

[Additional Information: full citation, abs](#)

**Bibliometrics:** Downloads (6 Weeks): 7, Downloads (12 Months): 58, Downloa


A key analysis developed for the compilation of parallel programs is *thr  
 escape analysis*, which determines what objects are accessed in more t  
 program ...

**Keywords:** Java, analysis precision, dynamic compilation, escape analy

# 13 Traffic modeling and proportional partial caching for peer-to-peer sys

Mohamed Hefeeda, Osama Saieh

December 2008 **IEEE/ ACM Transactions on Networking (TON)** , Volume

**Publisher:** IEEE Press  [Request Permissions](#)

Full text available:  Pdf (558.70 KB)


Additional Information: [full citation](#), [abs](#)

**Bibliometrics:** Downloads (6 Weeks): 20, Downloads (12 Months): 189, Downl


Peer-to-peer (P2P) file sharing systems generate a major portion of the increase in the future. We explore the potential of deploying proxy cach with the goal ...

**Keywords:** internet measurement, network protocols, peer-to-peer sys

#### 14 [The LOCKSS peer-to-peer digital preservation system](#)

 Petros Maniatis, Mema Roussopoulos, T. J. Giull, David S. H. Rosenthal, Ma February 2005 **Transactions on Computer Systems (TOCS)** , Volume 23 Is

**Publisher:** ACM  [Request Permissions](#)

Full text available:  Pdf (715.30 KB)


Additional Information: [full citation](#), [abs](#)

**Bibliometrics:** Downloads (6 Weeks): 21, Downloads (12 Months): 156, Downl

The LOCKSS project has developed and deployed in a world-wide test a journals and other archival information published on the Web. It consist persistent Web caches ...

**Keywords:** Rate limiting, digital preservation, replicated storage

#### 15 [The quest for scalable support of data-intensive workloads in distribu](#)

 Ioan Raicu, Ian T. Foster, Yong Zhao, Philip Little, Christopher M. Moretti, June 2009 **HPDC '09: Proceedings of the 18th ACM international sympo**

computing

**Publisher:** ACM  [Request Permissions](#)

Full text available:  Pdf (1.28 MB)

Additional Information: [full citation](#), [abs](#)

**Bibliometrics:** Downloads (6 Weeks): 18, Downloads (12 Months): 217, Downl

Data-intensive applications involving the analysis of large datasets offer resources, for which data locality can be crucial to high throughput and approach that ...

**Keywords:** data diffusion, data management, data-aware scheduling, f

#### 16 [Managing prefetch memory for data-intensive online servers](#)

Chuanpeng Li, Kai Shen

December 2005 **FAST'05: Proceedings of the 4th conference on USENIX Technologies - Volume 4** , Volume 4

**Publisher:** USENIX Association


Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downl

Data-intensive online servers may contain a significant amount of prefe


I/O prefetching and high execution concurrency. Using a traditional access  
reclamation policy, memory ...

### 17 [New results on web caching with request reordering](#)

 Susanne Albers

June 2004 **SPAA '04**: Proceedings of the sixteenth annual ACM symposium on architectures

**Publisher:** ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (186.52 KB)


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**Bibliometrics:** Downloads (6 Weeks): 2, Downloads (12 Months): 19, Downloads


We study web caching with request reordering. The goal is to maintain requests that  
can be served at low cost. To improve cache hit rates, a limited number of  
[6], who recently ...

**Keywords:** approximation, batch, cache, competitive, document, offline

### 18 [A concurrent dynamic analysis framework for multicore hardware](#)

 Jungwoo Ha, Matthew Arnold, Stephen M. Blackburn, Kathryn S. McKinley  
October 2009 **OOPSLA '09**: Proceedings of the 24th ACM SIGPLAN conference on  
languages and applications

**Publisher:** ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (1.26 MB)

[Additional Information: full citation, abstract](#)

**Bibliometrics:** Downloads (6 Weeks): 27, Downloads (12 Months): 187, Downloads

Software has spent the bounty of Moore's law by solving harder problem domains  
level languages, virtual machine technology, binary rewriting, and dynamic  
more productive and programs ...

**Keywords:** dynamic analysis, instrumentation, multicore, profiling

Also published in:


October 2009 **SIGPLAN Notices** Volume 44 Issue 10

### 19 [Phase-based cache reconfiguration for a highly-configurable two-level](#)

 Ann Gordon-Ross, Jeremy Lau, Brad Calder

May 2008 **GLSVLSI '08**: Proceedings of the 18th ACM Great Lakes symposium on

**Publisher:** ACM  [Request Permissions](#)

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**Bibliometrics:** Downloads (6 Weeks): 2, Downloads (12 Months): 53, Downloads

Phase-based tuning methodologies specialize system parameters for each application  
are varied during execution, as opposed to remaining fixed as in an application  
and logic suggests ...

**Keywords:** cache tuning, caches, configurable architecture, configurable  
reconfiguration, phase-based tuning



# Towards practical page coloring-based multicore cache management

Xiao Zhang, Sandhya Dwarkadas, Kai Shen

April 2009 **EuroSys '09: Proceedings of the 4th ACM European conference on**

**Publisher:** ACM [Request Permissions](#)

Full text available: Pdf (1.03 MB)

[Additional Information: full citation, abstract](#)

**Bibliometrics:** Downloads (6 Weeks): 19, Downloads (12 Months): 257, Downloads (Overall): 1,038

Modern multi-core processors present new resource management challenges. This paper presents a new resource management technique that simultaneously executing processes sharing on-chip resources (particularly cache) and demonstrates that the operating system ...

**Keywords:** cache partitioning, multi-core, page coloring, resource management

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